

02570 U.S. PTO
09/24/03
FR

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September 24, 2003

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Attorney Docket No.: 10559/855001/P17303/Intel Corporation

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Presented for filing is a new original patent application of:

FR

BOSTON

DALLAS

DELAWARE

NEW YORK

SAN DIEGO

SILICON VALLEY

TWIN CITIES

WASHINGTON, DC

Applicant: VALERY M. DUBIN
Title: NANOTUBES FOR INTEGRATED CIRCUITS

Enclosed are the following papers, including those required to receive a filing date under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	14
Claims	8
Abstract	1
Declaration	[To be Filed at a Later Date]
Drawing(s)	6

Enclosures:

- New disclosure information, including:
Information disclosure statement, 2 pages.
PTO-1449, 1 pages.
References, 4 items.
- Postcard.

There are 33 total claims, 4 of which are independent.

Basic filing fee	\$0
Total claims in excess of 20 times \$18	\$0
Independent claims in excess of 3 times \$84	\$0
Fee for multiple dependent claims	\$0
Total filing fee:	\$0

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No filing fee is being paid at this time.

If this application is found to be incomplete, or if a telephone conference would otherwise be helpful, please call the undersigned at (858) 678-5070.

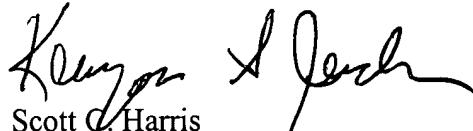
Kindly acknowledge receipt of this application by returning the enclosed postcard.

Please direct all correspondence to the following:

20985

PTO Customer Number

Respectfully submitted,



Scott C. Harris

Reg. No. 32,030

Attorney for Intel Corporation

Enclosures

SCH/rpi

10332379.doc

/BY
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